

Fig. 1 is a block diagram of a radio receiver circuit. The circuit is divided into three main sections: 1 (Input Stage), 2 (Intermediate Frequency Stage), and 3 (Output Stage). Section 1 includes an input tuning circuit (14) with a variable capacitor (14a) and a variable inductor (14b). Section 2 includes an inter-stage tuning circuit (14c) with a variable capacitor (14d) and a variable inductor (14e). Section 3 includes an output tuning circuit (14f) with a variable capacitor (14g) and a variable inductor (14h). The circuit is powered by a power supply (10) and a speaker (11).

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FIG. 2  
PRIOR ART

